

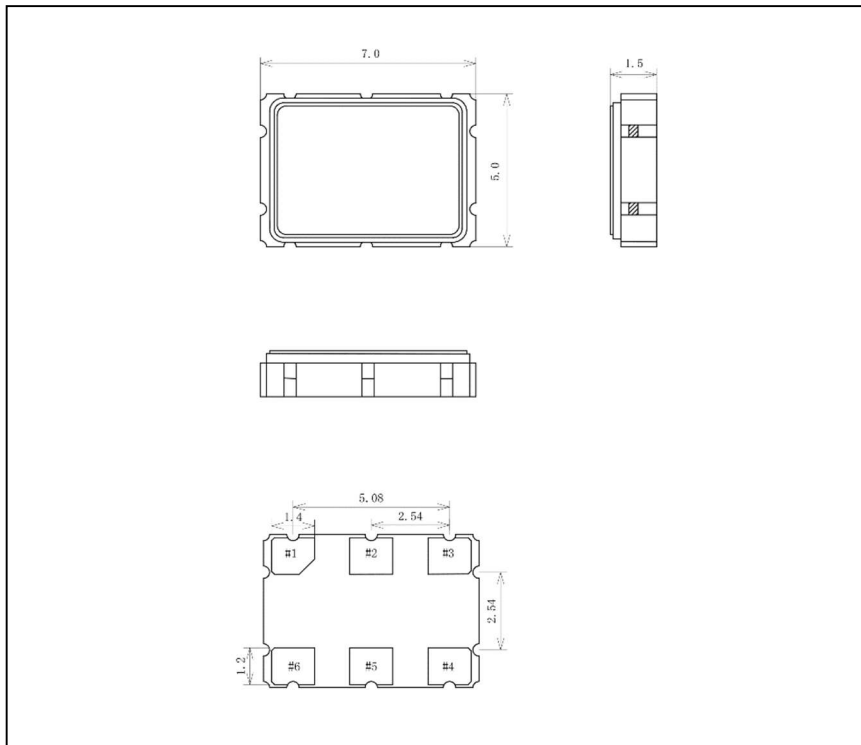
## Ultra Low Phase Noise SMD Crystal Oscillator

Part No.	AOY-L7050LP
Our No.	
Quantity.	
Logic	LVPECL

**TENTATIVE**

Package size:	7.0mm*5.0mm
Frequency:	200.000MHz
Supply Voltage:	3.3Vdc±10%
Frequency Stability:	<b>±50ppm</b>
Operating Temp.:	-40 to +85 deg C
Storage Temp.:	-55 to +125 deg C
Supply current:	65mA max.
Symmetry:	45/55%
Rise & Fall time:	0.4ns max.
Output Load:	50Ω into V <sub>DD</sub> -2V
Start-up time:	10ms max.
Output voltage:	V <sub>OH</sub> =V <sub>DD</sub> - 1.040V min. V <sub>OL</sub> =V <sub>DD</sub> - 1.620V max.
Tristate:	pin #1                  Pin#4.#5 open                    Active, ≥0.7 V <sub>DD</sub> Active ≤0.3 V <sub>DD</sub> High Z
RMS Phase Jitter	0.1ps max. (Integrated 12kHz to 20MHz)

### CASE DIMENSION



### PIN CONNECTIONS

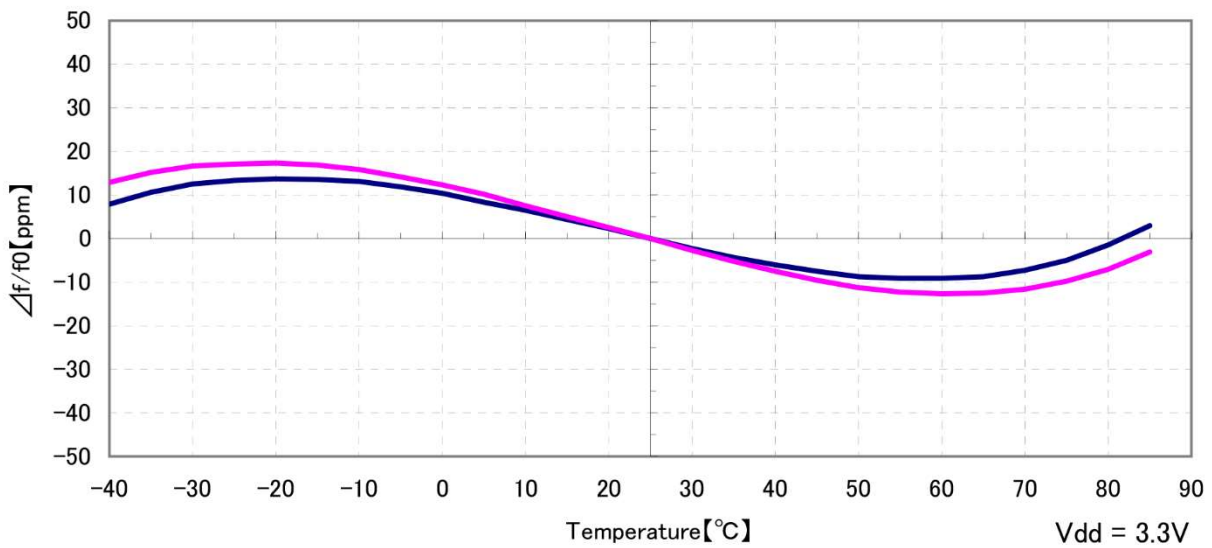
pin #1	E/D
pin #2	NC
pin #3	GND
pin #4	OUT
Pin #5	OUT N
Pin #6	V <sub>DD</sub>

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Frequency Stability vs Temp.



### Phase Noise

- 129dBc/Hz@1kHz
- 157dBc/Hz @100KHz
- 161dBc/Hz @1MHz
- 162dBc/Hz @10MHz
- 162dBc/Hz @40MHz

### RMS Jitter

38fs(Typ.) /12kHz to 20MHz

